

ABSTRACT OF THE DISCLOSURE

In a shared bus connection scheme or an individual path connection scheme, a fault caused at a part of a system affects the same in its entirety. Also, these schemes do not permit the determination of fault locations. Adapters 11, 12 are connected to shared memories 21, 22 with a plurality of individual paths 31 through 38. An address locking unit (not shown) is arranged in each of the shared memories 21, 22 to perform address lock check on memory access operations from the adapters 11, 12. If an address to be accessed is in the locked state, the access is held in the lock wait state until the address is unlocked. The access is made when the address locking is cleared.